ABSTRACT OF THE DISCLOSURE

In a gain control device for packet signal receiver, a variable gain amplifier amplifies an input signal with a gain corresponding to a control voltage applied thereto, and a power detector detects output power of the variable gain amplifier. A packet detection circuit detects a packet signal based on the detected output power. A control circuit outputs the control voltage variable with the detected output power, and the control voltage is provided for the amplifier. Thus high-speed gain control is performed immediately after the start of detection of the packet signal. When the elapsed time after the start of detection of the packet signal exceeds a predetermined time, a sample-hold circuit sample-and-holds the control voltage. This control voltage is provided for the amplifier up to the end of reception of the packet signal thereafter. Thus low-speed gain control is performed to provide stable power without distorting the signal wave.